IN THE SPECIFICATION

Please amend the specification as follows. Assignee respectfully submits that no new subject matter is added.

After paragraph 0015, please ADD the following paragraph:
[0015.1] FIG. 5 is a block diagram of an exemplary circuit for decoding video data in accordance with an embodiment of the present invention.

After Paragraph 0023, please ADD the following paragraph:
[0023.1] Referring now to **FIGURE 5**, there is illustrated a block diagram describing an exemplary circuit 500 for decoding video data in accordance with an embodiment of the present invention. A video decoder 505 includes a function commonly referred to as “motion compensation.” This function is used to allow the video decoder 505 to process numerous video compression standards, including but not necessarily limited to: MPEG-1, MPEG-2, MPEG-4, H.263, H.264, and H.261. More specifically, motion compensation includes a process of copying a two-dimensional block 510 of image data from a previously decoded reference frame 515 to the frame 520 currently being decoded. The location of the reference block relative to the current position in the current frame is specified by "motion vectors" included within the input code stream 525. Motion compensation allows for a compact specification of the data whenever the video stream is well modeled by translational motion. The reference frame 515 that is used for motion compensation is stored in a relatively large memory, such as DRAM 530. The DRAM 530 can be configured to store the reference frame 515 in the manner described in FIGURES 3 and 4. In one

embodiment, segments of data with better alignment with respect to the burst boundaries can be determined by combinatorial logic in the video decoder 505.

Please REPLACE paragraph 0024 with the following paragraph:

[0024] ~~In one particular instance, an implementation can be applied to any video decoder and related encoder requiring motion compensation. In one embodiment, H.261 codecs are used as decoders.~~ In general, the technique can improve read efficiency significantly in circuit 500 during motion compensation. ~~any burst oriented memory subsystem (video codec or other) that has reads which required a relatively small number of bursts~~.

Please REPLACE paragraph 0025 with the following paragraph:

[0025] The methods described above may be implemented using one or more data processing devices. In some embodiments, the data processing devices may implement the functionality of the present invention in hardware, using, for example, a computer chip. The data processing device μP may receive signals in analog or digital form. In other embodiments, the data processing device μP may implement the functionality of the present invention as ~~software~~ machine readable memory storing a plurality of executable instructions on a general purpose computer, video display device, or other electronic device. In such an embodiment, the program may be written in any one of a number of programming languages, such as FORTRAN, PASCAL, C, C++, C#, Tcl, or BASIC. Further, the program can be written in a script, macro, or functionality embedded in commercially available software, such as EXCEL or VISUAL BASIC.